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(6 citations)

University of Toronto. Region-Oriented Main **Memory Management** in Shared-Memory NUMA Multiprocessors
ftp.cs.toronto.edu/pub/parallel/Gamsa_MASc.ps.Z

Java Operating Systems: Design and Implementation - Back, Tullmann, Stoller.. (1998) (Correct) (38 citations)
systems such as Java use type safety to provide **memory** safety in a single address space. **Memory** safety
a process model that enables the control and **management** of computational resources. In particular,
mancos.cs.utah.edu/papers/javaos-tr98015.ps.gz

Cyclic Weighted Reference Counting without Delay - Jones, Lins (1992) (Correct) (14 citations)
can be performed simultaneously. Keywords: **Memory management**, Distributed **memory**, Reference
be performed simultaneously. Keywords: **Memory management**, Distributed **memory**, Reference counting,
unix.hensa.ac.uk/pub/misc/ukc.reports/comp.sci/reports/28-92.ps.Z

Networking Implementation Notes 4.BSD Edition - Leffler, Joy, Fabry, Karels (Correct)
3. Goals 4. Internal address representation 5. **Memory management** 6. Internal layering 6.1. Socket
4. Internal address representation 5. **Memory management** 6. Internal layering 6.1. Socket layer 6.1.1.
ftp.riken.go.jp/pub/NetBSD/misc/lite2-docs/smm/18.net.ps.gz

VINO: An Integrated Platform for Operating System and.. - Small, Seltzer (1994) (Correct) (17 citations)
control of the DBMS. However, because the virtual **memory** of the database process is still managed by the
of traditional and modern database **management** systems. 1 Introduction In general, operating
hpc.ee.ntu.edu.tw/~murphy/reports/rtos/papers/vino-tr-30-94.ps.gz

Low Power TLB Design for High Performance Microprocessors - Manne, Klauser, Grunwald.. (1997) (Correct)
(4 citations)
Much of the energy consumption comes from the **memory** hierarchy of the processor. Previous studies [10]
space protection and flexibility in **memory management**. In operating systems supporting
bessie.colorado.edu/personal/bobbie/Papers/islped97.ps

Mach: A Foundation for Open Systems - Rashid, Baron, Forin, Golub.. (1989) (Correct) (40 citations)
the **management** of CPU, communication, virtual **memory** and secondary storage resources in a way that
system and user applications. It exposes the **management** of CPU, communication, virtual **memory** and
ftp.cs.cmu.edu/project/mach/doc/published/intro.ps

Cooperative Caching in Append-only Databases with Hot Spots - Aman Sinha (Correct)
as follows: Section 2 discusses work related to **memory management** in both the operating systems and
server? We describe and compare several cache **management** policies to study these issues. In each case,
lore.ece.utexas.edu/~sinha/ICDE.ps

Tempest and Typhoon: User-Level Shared Memory - Reinhardt, Larus, Wood (1994) (Correct) (243 citations)
Tempest and Typhoon: User-Level Shared **Memory** Steven K. Reinhardt, James R. Larus, and David A.
ftp.cs.wisc.edu/www/isca94_typhoon.ps

From Region Inference to von Neumann Machines via.. - Birkedal, Tofte.. (1996) (Correct) (73 citations)
calculus can be implemented using regions for **memory management**[17]At runtime, the store consists of
can be implemented using regions for **memory management**[17]At runtime, the store consists of a stack
ftp.diku.dk/diku/semantics/papers/D-313.ps.gz

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[Bus Minimization and Scheduling of Multi-Chip Systems - Sheliga, Sha \(Correct\)](#)

Abstract This paper considers several different **algorithms** that reduce the required number of buses for module design. An efficient polynomial time **algorithm** that calculates the minimum number of buses schedule is presented. We also present three **algorithms** that minimize the number of buses during
www.nd.edu/~esha/papers/mike/bus_gls6.ps

[Run-Time Parallelization Of Irregular Doacross Loops - Jeyaraman, Krothapalli.. \(1996\) \(Correct\)](#)

presented the performance of the **algorithm** on a **collection** of distributed networked heterogeneous to perform a run-time analysis. We describe a new **algorithm** to perform this analysis. The proposed method to design effective run-time parallelization **algorithms** [2,3,4,5,6,7,8,9,10]The main differences
ftp.csd.uwo.ca/pub/mwg/doacross.ps

[Model Reduction from an H1/LMI perspective - Helmersson \(Correct\)](#)

norm reduction. As an intermediate step, the **algorithm** finds a generalized balanced realization such and Hankel norm reduction [6] are well-behaved **algorithms** for finding reduced order models. In this paper cases (if $k \ln \Gamma$)The model reduction **algorithm** proposed is based on an iterative two-step LMI
ftp.control.isy.liu.se/pub/Reports/1994/1690.ps.Z

[Parallel Algorithms for High-dimensional Proximity Joins - Shafer, Agrawal \(1997\) \(Correct\) \(4 citations\)](#)

Parallel **Algorithms** for High-dimensional Proximity Joins John C.

We present a parallel multidimensional join **algorithm** based on an the epsilon-kdB tree and compare it Of Space Partitioning. An Evaluation Of The **Algorithms** On An Ibm Sp2 Shared-Nothing Multiprocessor Is
www.almaden.ibm.com/cs/people/ragrawal/papers/vldb97_ekdb.ps

[Calculation of Zero Dynamics using the Ritt Algorithm - Fortell \(1994\) \(Correct\)](#)

Calculation of Zero Dynamics using the Ritt **Algorithm** Hakan Fortell Department of Electrical how differential algebra, in particular the Ritt **algorithm**, can be used to calculate zero dynamics of a that for affine polynomial SISO systems the Ritt **algorithm** gives a result which is equivalent to the
ftp.control.isy.liu.se/pub/Reports/1994/1585.ps.Z

[Porting the Galaxy System to Mandarin Chinese - Wang \(1997\) \(Correct\) \(2 citations\)](#)

Pao, and Ed Hurley, for their assistance in data **collection** and maintaining the system Vicky Palay and . 50 3.2.3 City-state Decoding **Algorithm** .52 3.3 . 38 2-9 **Algorithm** to correct type 2 homophones .
www.sls.lcs.mit.edu/sls/publications/1997/synthesis-wangc.ps.gz

[Sparse coding with an overcomplete basis set: A strategy.. - Olshausen, Field \(1998\) \(Correct\) \(103 citations\)](#)

so that images may be explained in terms of a **collection** of independent events. The hope is that such a the simulation and results obtained applying the **algorithm** to natural images are described in Sections 6 the model, as well as the relation between our **algorithm** and other efficient coding methods that have
redwood.ucdavis.edu/pub/papers/VR.ps.Z

[Analysis of Approximate Nearest Neighbor Searching with.. - Maneewongvatana, Mount \(1999\) \(Correct\) \(1 citation\)](#)

is given not only the data points, but also a **collection** of sample query points, called the training with dimension. The difficulty of obtaining **algorithms** that are efficient in the worst case with been heavily studied recently. Examples include **algorithms** by Bern [8]Arya and Mount [2]Arya, et al.
ftp.cs.umd.edu/pub/faculty/mount/Papers/dimacs99.ps.gz

[Comprehension Syntax - Buneman, Libkin, Suciu, Tannen, Wong \(1994\) \(Correct\) \(42 citations\)](#)

syntax that deals uniformly with a variety of **collection** types it also includes pattern matching,

[29] D. Suciú and J. Paredaens. Any **Algorithm** in the Complex Object Algebra Needs 1991. 34] A. Wijngaarden. Revised Report on the **Algorithmic** Language ALGOL 68. Acta Informatica, sdmc.krdl.org.sg/kleisli/psZ/blstwsigmodrecord94.ps

A Statically Safe Alternative to Virtual Types - Bruce, Odersky, Wadler (1998) (Correct) (33 citations)
Parametric types are especially useful for **collection** types, such as lists or sets. Users of C
www.cs.bell-labs.com/~wadler/topics/./papers/parvsvirt/parvsvirt.ps.gz

A Temporal Extension to a Generic Object Data Model - Steiner, Norrie (1997) (Correct) (1 citation)
are represented by the bulk type constructor **collection** and classification structures are built from
www.timeconsult.com/Literature/tom.ps

I/O Optimal Isosurface Extraction (Extended Abstract) - Chiang, Silva (Correct)
[9] propose a method based on identifying a **collection** of seed cells from which isosurfaces can be
grids. We show that, in practice, our **algorithms** improve the performance of isosurface
depending on the type of cells, one can apply an **algorithm** to actually generate the isosurface from those
cis.poly.edu/chiang/iso-vis97.ps.gz

Optimized Software Synthesis for Digital Signal.. - Jürgen Teich.. (1998) (Correct) (3 citations)
relative to the Evolutionary **Algorithm** on our **collection** of CHAPTER 4. EXPERIMENTS 23 1500 Fitness
Software Synthesis for Digital Signal Processing **Algorithms** -An Evolutionary Approach Jurgen Teich and
.9 2.2 Why Using an Evolutionary **Algorithm** .10 2.3
ftp.tik.ee.ethz.ch/pub/people/zitzler/TZB1998a.ps.gz

Learning in Case-Based Classification Algorithms - Globig, Wess (1995) (Correct) (1 citation)
Learning in Case-Based Classification **Algorithms** Christoph Globig, Stefan Wess University of
To do so, we transform a simple symbolic learning **algorithm** (the version space **algorithm**) into an
symbolic learning **algorithm** (the version space **algorithm**) into an equivalent case-based variant. The
kbibmp3.ub.uni-kl.de/Preprint_Informatik/PS/no_series_174.ps.gz

Color-Based Content Coding with Applications to Sign.. - Schumeyer, Barner (1997) (Correct)
that encompass these features. Two segmentation **algorithms** are presented: the first uses a static region
and hands in real-time. The dynamic segmentation **algorithm** identifies flesh regions using statistical
at a the same bit rate when compared to a uniform **algorithm**. Permission to publish this abstract separately
www.asel.udel.edu/sem/research/speech/tcsvt.ps.gz

A Global Convergence Theory for Sequential Linear Programming.. - Hallabi (1994) (Correct)
for Sequential Linear Programming Inexact Hybrid **Algorithms** Mohammadi El Hallabi CRPC-TR94371 January
1994
Convergence Theory for SLP and SQP Trust-Region **Algorithms**. Also available as CAAM-TR95-08 from the
For Sequential Linear Programming Inexact Hybrid **Algorithms** 1 Mohammadi El Hallabi 2 Abstract. In This
softlib.rice.edu/pub/CRPC-TRs/reports/CRPC-TR94371.ps.gz

On Lazy Randomized Incremental Construction - de Berg, Dobrindt, Schwarzkopf (1995) (Correct) (9 citations)
in the plane. For every subset $R \subseteq S$, define a **collection** of "regions" $C(R)$ The set of line segments R
introduce a new type of randomized incremental **algorithms**. Contrary to standard randomized incremental
Contrary to standard randomized incremental **algorithms**, these lazy randomized incremental **algorithms**
www.cs.ust.hk/~otfried/Papers/1994-12.ps

Strategic Oscillation in Heuristic Local Search - Laurent Mynard (Correct)
Abstract This paper describes a local search **algorithm** for combinatorial optimization problems. It is
knapsack problem. Experience proves that the **algorithm** is very efficient, as well according to the
based on three main technics. Firstly specific **algorithms** are designed to solve one given problem by
www.poleia.lip6.fr/~mynard/frames/./ps/kbcs96.ps.gz

Automatic Configuration of Parallel Programs for.. - Hluchý, Dobrovodský.. (Correct)
Abstract: This paper describes the mapping **algorithm** for distributed memory, parallel message
is duration of the iteration step of the mapping **algorithm**. $Fvertex(M,t)$ expresses the effect of
influence of communications becomes weaker. The **algorithm** can be implemented in the distributed
ups.savba.sk/parcom/sephp/docs/parco4r.ps.gz

PSATO: a Distributed Propositional Prover and Its.. - Zhang, Bonacina, Hsiang (1996) (Correct) (9 citations)
Stinson, D. eds)Contemporary Design Theory: A **Collection** of Surveys. Boehm, M.Speckenmeyer, E. 1994)
an efficient implementation of the Davis-Putnam **algorithm**. The masterslave model is used for
(ii) designing highly scalable parallel **algorithms** and (iii) supporting "fault-tolerant"
www.cs.uiowa.edu/ftp/hzhang/sato/papers/jscpsato.ps.Z

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using a specialized library, namely PVM (Parallel **Virtual Machine**) An example is given :the LBG specialized library, namely PVM (Parallel **Virtual Machine**) An example is given :the LBG **algorithm** for efficiently with time-consuming image processing **algorithms**. It is shown how to distribute the processes, ns1.tele.ucl.ac.be/PEOPLE/PP/lbgparal.ps.gz

[A Syntactic Framework For Bitstream-Level Representation Of... - Yihan Fang \(1996\) \(Correct\)](#)

languages using **virtual machines**, most notably Java [7, 8] the language would have the advantage of coding. It should be compile source code **virtual machine** code **virtual machine** interpreter and thoroughly defined semantics suitable for **machine** translation. This work is currently part of the www.cnmtc.columbia.edu/~eleft/papers/icip96-yf.ps.gz

[PVM-Prolog: Parallel Logic Programming in the PVM System - Rui Marques \(1995\) \(Correct\) \(2 citations\)](#)PVM Interface Layer 7 4.1 Accessing a parallel **virtual machine** from Prolog :7 4.2Layer 7 4.1 Accessing a parallel **virtual machine** from Prolog :7 4.2 Summary of

www.soi.city.ac.uk/~msch/pvm/marques.ps.gz

[Composition Validation and Subjectivity in GenVoca Generators - Batory, Geraci \(1997\) \(Correct\) \(30 citations\)](#)

defined by a series of progressively more abstract **virtual machines** [Dij68] A **virtual machine** is a set of a series of progressively more abstract **virtual machines** [Dij68] A **virtual machine** is a set of classes, present simple, efficient, and domainindependent **algorithms** for validating compositions of GenVoca ftp.cs.utexas.edu/pub/predator/ieee-icsr.ps

[On The Granularity And Clustering Of Directed Acyclic Task.. - Gerasoulis, Yang \(1990\) \(Correct\) \(47 citations\)](#)

number of processors on a completely connected **virtual** architecture. Sarkar calls this step the Task Scheduling over Distributed Memory **Machines**, Proc. of the International Workshop on Parallel with a performance bound for linear clustering **algorithms**, shows that linear clustering is the best www.cs.rutgers.edu/pub/gerasoulis/reports/LCSR-TR-153.ps.Z

[End-to-End Delay Bounds and Buffer Sizing in ATM Networks - Hung And \(1995\) \(Correct\) \(1 citation\)](#)

we obtain end-to-end delay bounds over arbitrary **virtual** circuits of such schedulers. For (oe cheetah.vlsi.uwaterloo.ca/~kesidis/End-to-End.ps

[The VEOS Project - Bricken, Coco \(1993\) \(Correct\) \(14 citations\)](#)

it creates with its own interface. One solution, **virtual** reality (VR) immediately raises fundamental www.hitl.washington.edu/publications/r-93-3//r-93-3.ps

[CalREN ATM testbed Deployment: A Report - Senthil Sengodan \(Correct\)](#)

the user different bit rates of transport. 2.2 **Virtual** Path and **Virtual** Channel Since ATM is the demonstrations to connect to SGI Indigo **machines** at the WBIT booth. The ASX-100 can support both commsci.usc.edu/~sengodan/pubs/calren.ps

[STALK: An Interactive Virtual Molecular Docking System - Levine, Facello.. \(1996\) \(Correct\)](#)STALK: An Interactive **Virtual** Molecular Docking System David Levine

x Abstract Several recent technologies-genetic **algorithms**, parallel and distributed computing, **virtual** of molecular interactions. Parallel genetic **algorithms** are an efficient and effective means to explore info.mcs.anl.gov/pub/tech_reports/reports/P603.ps.Z

[Supporting Transcontinental Collaborative Work in Persistent.. - Leigh, Johnson \(1996\) \(Correct\) \(6 citations\)](#)Transcontinental Collaborative Work in Persistent **Virtual** Environments Jason Leigh and Andrew E. Johnson

<http://citeseer.ist.psu.edu/cs?cs=1&q=java+virtual+machine++sarsa+algorithm&co=Citations...> 1/26/06

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IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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 Digital Object Identifier 10.1109/TPAMI.2005.201
[AbstractPlus](#) | Full Text: [PDF](#)(1872 KB) IEEE JNL
- ☐ 2. **Q-learning-based multirate transmission control scheme for RRM in mult systems**
 Yih-Shen Chen; Chung-Ju Chang; Fang-Chin Ren;
 Vehicular Technology, IEEE Transactions on
 Volume 53, Issue 1, Jan. 2004 Page(s):38 - 48
 Digital Object Identifier 10.1109/TVT.2003.822330
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(360 KB) IEEE JNL
- ☐ 3. **Q-DPM: an efficient model-free dynamic power management technique**
 Min Li; Xiaobo Wu; Yao, R.; Xiaolang Yan;
 Design, Automation and Test in Europe, 2005. Proceedings
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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard



1. Progress in learning 3 vs. 2 keepaway

Kuhlmann, G.; Stone, P.;

Systems, Man and Cybernetics, 2003. IEEE International Conference on Volume 1, 5-8 Oct. 2003 Page(s):52 - 59 vol.1

Digital Object Identifier 10.1109/ICSMC.2003.1243791

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Rezaei, M.; Kavi, K.M.;
Southeastcon 2000. Proceedings of the IEEE
7-9 April 2000 Page(s):332 - 339
Digital Object Identifier 10.1109/SECON.2000.845587
AbstractPlus Full Text: PDF (548 KB) IEEE CNF |
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Basanta-Val, P.; Garcia-Valls, M.; Estevez-Ayres, I.;
Object-Oriented Real-Time Distributed Computing, 2005. ISORC 2005. Eighth International Symposium on
18-20 May 2005 Page(s):382 - 389
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Chang, J.M.; Srisa-An, W.; Chia-Tien Dan Lo;
High Performance Computing in the Asia-Pacific Region, 2000. Proceedings. 1 International Conference/Exhibition on
Volume 1, 14-17 May 2000 Page(s):513 - 517 vol.1
Digital Object Identifier 10.1109/HPC.2000.846607
AbstractPlus Full Text: PDF (396 KB) IEEE CNF |
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Dingle, A.; Hildebrandt, T.H.;
Technology of Object-Oriented Languages and Systems, 1997. TOOLS 23. Pr
28 July-1 Aug. 1997 Page(s):38 - 47
Digital Object Identifier 10.1109/TOOLS.1997.654699
AbstractPlus Full Text: PDF (52 KB) IEEE CNF |
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Nilsen, K.D.; Hong Gao;
Real-Time Technology and Applications Symposium, 1995. Proceedings
15-17 May 1995 Page(s):142 - 153
Digital Object Identifier 10.1109/RTTAS.1995.516211
AbstractPlus Full Text: PDF (1136 KB) IEEE CNF |

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Harbaugh, S.; Wavering, B.;
Aerospace and Electronics Conference, 1991. NAECON 1991., Proceedings of
National
20-24 May 1991 Page(s):704 - 708 vol.2
Digital Object Identifier 10.1109/NAECON.1991.165829
[AbstractPlus](#) | Full Text: [PDF](#)(360 KB) IEEE CNF
- ☐ **7. Lock-free garbage collection for multiprocessors**
Herlihy, M.P.; Moss, J.E.B.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 3, Issue 3, May 1992 Page(s):304 - 311
Digital Object Identifier 10.1109/71.139204
[AbstractPlus](#) | Full Text: [PDF](#)(732 KB) IEEE JNL
- ☐ **8. Active memory processor: a hardware garbage collector for real-time Java devices**
Srisa-an, W.; Lo, C.-T.D.; Chang, J.-M.;
Mobile Computing, IEEE Transactions on
Volume 2, Issue 2, Apr-Jun 2003 Page(s):89 - 101
Digital Object Identifier 10.1109/TMC.2003.1217230
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- ☐ **9. Complete distributed garbage collection: an experience with Rotor**
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IEEE CNF IEEE Conference Proceeding

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